

RICHARD SPENCER

Test Engineer | Medical Device Verification & Validation

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PROFESSIONAL SUMMARY

Test Engineer with 4+ years of medical device verification and validation experience in ISO 13485 and IEC 60601 regulated environments. Proven expertise in requirements traceability, test strategy development, and cross-functional collaboration on Class II medical devices. Strong track record of process improvement, including development of automated workflows that reduced documentation time by 50%. Skilled in V&V methodologies, integration testing, defect triage, and supporting regulatory compliance efforts. Currently completing B.S. in Computer Science (2027) while seeking roles emphasizing test planning, requirements analysis, and systems-level validation.

CORE COMPETENCIES

V&V Methodologies • Integration Testing • Requirements Traceability • Test Plan Development • Defect Triage • ISO 13485 • IEC 60601 • 21 CFR 1020 • MatrixALM • Jama • Jira • Cross-Functional Collaboration • Technical Documentation • Process Improvement • Python & Java (basic) • Docker/Compose

PROFESSIONAL EXPERIENCE

Test Engineer | Stryker, Shirley, MA

December 2024 – December 2025

Note: Position eliminated due to product line restructuring

- Led test execution for next-generation medical device, applying V&V methodologies aligned with ISO 13485 and IEC 60601 standards
- Collaborated in cross-functional systems engineering meetings to support requirements traceability, remediation planning, and test strategy development
- Validated system-level integration of products beyond original assignment scope, ensuring cross-compatibility within larger product ecosystem
- Supported technical documentation development by reviewing and revising protocols, procedures, and engineering reports
- Guided onboarding and knowledge transfer for junior technicians and contractors to maintain continuity in test development processes

Validation Engineer | Stryker, Shirley, MA

February 2024 – December 2024 (Contract)

- Developed and maintained test plans, protocols, and detailed reports for AIRO-related hardware/software systems
- Designed and deployed GUI-based evidence capture workflow that standardized file naming, folder structuring, and cloud delivery, reducing documentation time by 50% and eliminating manual errors
- Acted as liaison during third-party penetration testing, clarifying product workflows and facilitating successful setup and execution
- Led auxiliary system-level testing, contributing to successful validation of new functionality and design updates

- Participated in legacy software reviews, uncovering unresolved defects with potential clinical impact and contributing to risk mitigation planning
- Created automation scripts (Python/Java) and CLI tools to streamline connectivity checks and verification of system configurations

Quality Engineering Specialist | AMD, Boxborough, MA

November 2022 – November 2023 (Contract)

- Created and executed structured test plans, meeting aggressive weekly validation targets for high-performance hardware platforms
- Maintained traceable defect and results database tracking thousands of test cycles; identified patterns to improve test prioritization
- Documented and triaged defects using Jira and internal ticketing systems to ensure timely resolution and release readiness
- Partnered with ISV teams to optimize software compatibility and test automation support

Validation Technician | Stryker, Shirley, MA

September 2021 – November 2022 (Contract)

- Managed requirement traceability and test documentation in MatrixALM for Class II medical devices
- Supported early-phase concept testing for robotics prototypes in collaboration with R&D and systems engineering teams
- Executed detailed test protocols to evaluate system performance and compliance with internal quality standards
- Completed Level 1 Service Technician certification for AIRO TruCT imaging systems to enhance support during integration and validation
- Assisted in test automation strategies for TruCT platform, improving test coverage and reducing manual effort
- Coordinated cross-functional testing schedules to meet critical release gates

EDUCATION & PROFESSIONAL DEVELOPMENT

Bachelor of Science, Computer Science | Southern New Hampshire University | Expected 2027 | 4.0 GPA

Graphic Design Coursework | Mount Wachusett Community College | Dean's List (multiple years)

TECHNICAL INTERESTS

CAD Design & 3D Printing for Test Fixtures • Product Photography for Technical Documentation • Linux System Administration (Raspberry Pi) • Home Automation (Home Assistant, AdGuard Home)